

LESSON PLAN

Discipline: E&TC Engg.	Semester: Fourth(4 th)	Name of the Lab I/C: Er Debasmita Mohapatra
Subject: AE&LIC Lab (Pr 4)	No. of days/week class allotted: Three (3)	Semester from Date: 16.01.24 to Date: 26.04.24 No. of Weeks: 15
WEEK	CLASS DAY	PRACTICAL EXPERIMENTS
1 st	1 st	Determine the input and output characteristics of CE transistor configuration.
	2 nd	
	3 rd	Review Class
2 nd	1 st	To construct bridge rectifier using different filter and to determine Ripple Factor & analysis the wave form with filter and without filter.
	2 nd	
	3 rd	Review Class
3 rd	1 st	To construct Bridge rectifier using different filter & to determine Ripple Factor.
	2 nd	
	3 rd	Review Class
4 th	1 st	To study the single stage common emitter amplifier & Find Gain.
	2 nd	
	3 rd	Review Class
5 th	1 st	To study about multi stage R-C coupled amplifier & to determine frequency response & Gain.
	2 nd	
	3 rd	Review Class
6 th	1 st	To study multivibrator (Astable, Bistable & Monostable) circuit & draw its Waveforms.
	2 nd	
	3 rd	Review Class
7 th	1 st	To determine Drain and transfer characteristics of JFET.
	2 nd	
	3 rd	Review Class

8 th	1 st	Construct & find the gain of Class A , Class B & Class C Amplifier.
	2 nd	
	3 rd	Review Class
9 th	1 st	Construct & test push pull amplifier & observe the waveform.
	2 nd	
	3 rd	Review Class
10 th	1 st	Mini Project
	2 nd	
	3 rd	
11 th	1 st	Revision
	2 nd	
	3 rd	
12 th	1 st	Revision
	2 nd	
	3 rd	
13 th	1 st	Revision
	2 nd	
	3 rd	
14 th	1 st	Revision
	2 nd	
	3 rd	
15 th	1 st	Revision
	2 nd	
	3 rd	